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15. (Thrice Amended) A hybrid integrated circuit device comprising:
a hybrid integrated circuit substrate in which at least a surface is provided with insulation;
at least a first electrode and at least a second electrode formed on said surface and being
disposed to cover substantially the substrate;
a light emitting element connected with the first and second electrodes, and said first and
second electrodes configured to reflect light;
a seal which is disposed in a periphery of said substrate; and
a transparent substrate which is fixed to said hybrid integrated circuit substrate via
said seal to enclose the first and second electrodes and the light emitting element within a
sealed space formed between said hybrid integrated circuit substrate and said transparent
substrate, wherein the hybrid integrated circuit substrate is made of glass.

Please add claims 19 to 23.

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19. (New) A circuit device comprising:
a first substrate in which at least a surface is provided with insulation;
a first electrode and a second electrode formed on said surface;
a light emitting element connected to the first and second electrodes;
a driving circuit for driving said light emitting element;
a seal disposed in a periphery of said first substrate;
a second substrate comprising a transparent substrate which is fixed to said first substrate
through said seal to enclose said first and second electrodes, said light emitting element and said
driving circuit within a sealed space formed between said first substrate and said second
substrate; and
a spacer to keep the first and the second substrates apart.

20. (New) The circuit device according to claim 19, wherein said first substrate
comprises a glass substrate or a transparent substrate.

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21. (New) The circuit device according to claim 20, wherein said second substrate is made of glass, and said spacer keeps a distance between said first substrate and said second substrate constant.

22. (New) The circuit device according to claim 19, wherein a gas for preventing said light emitting element and said driving circuit from deteriorating is filled into the sealed space formed between said first substrate and said second substrate.

23. (New) The circuit device according to claim 19, wherein said first substrate comprises a hybrid integrated circuit substrate.
